FIG. 1

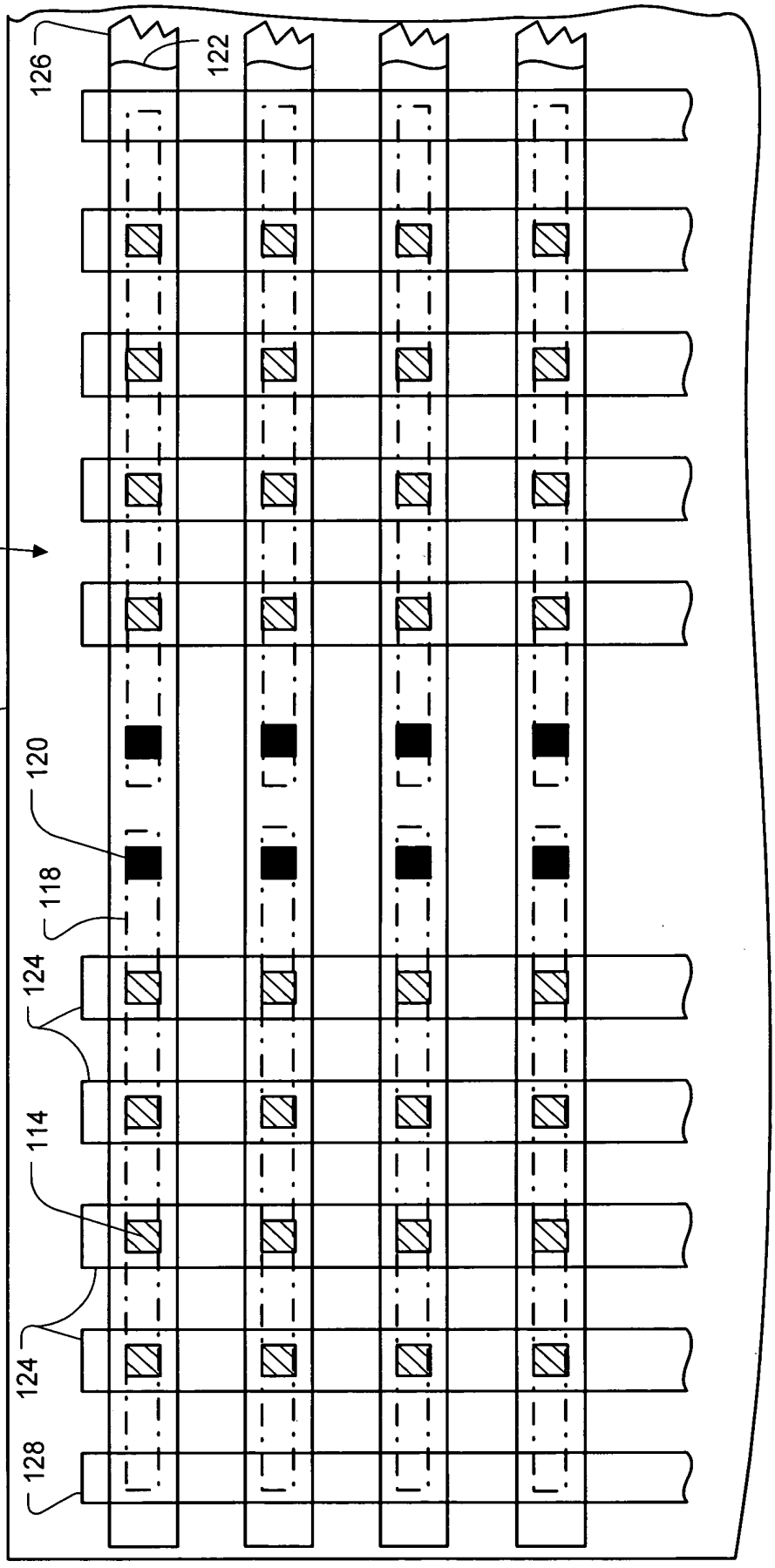


FIG. 2

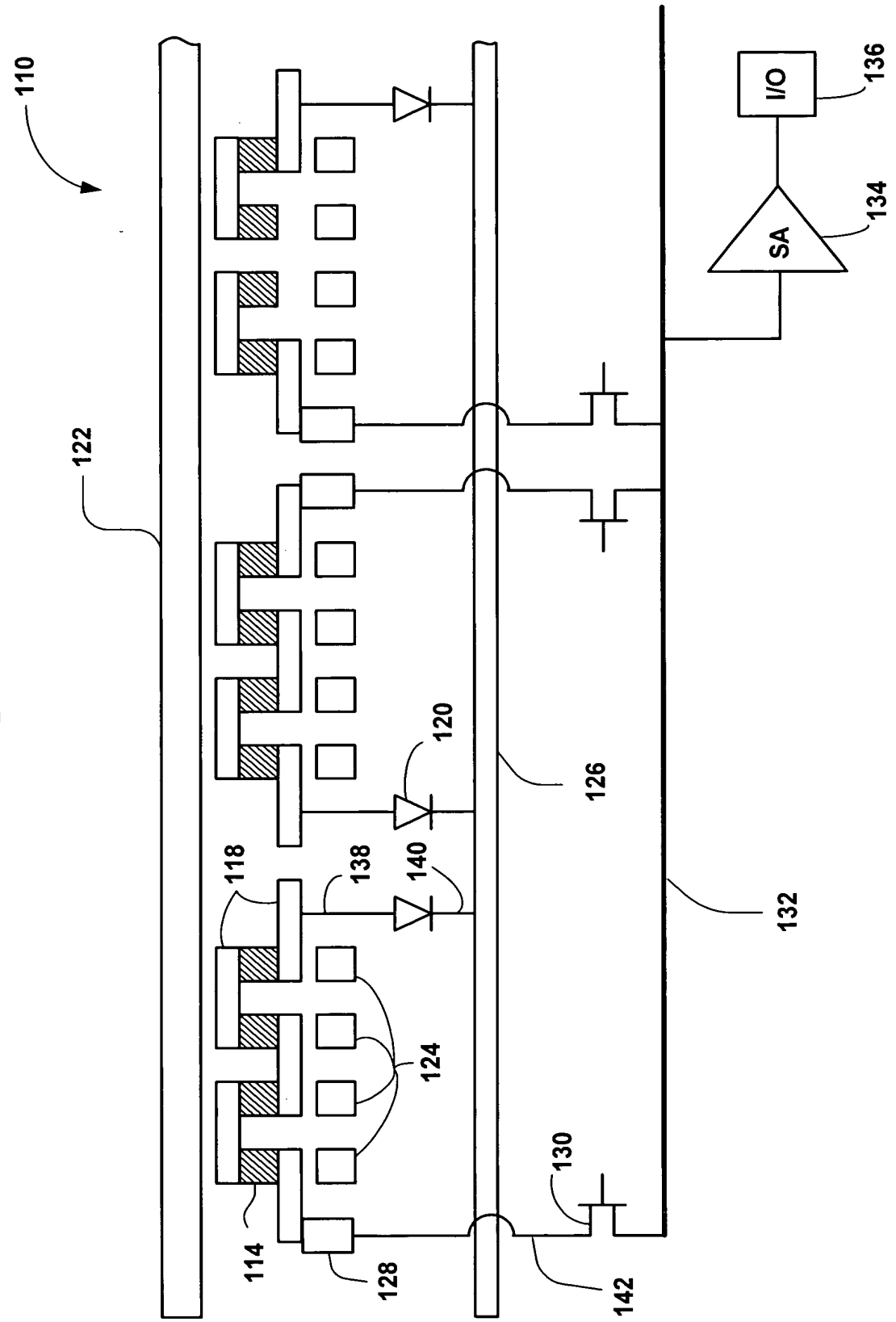


FIG. 3

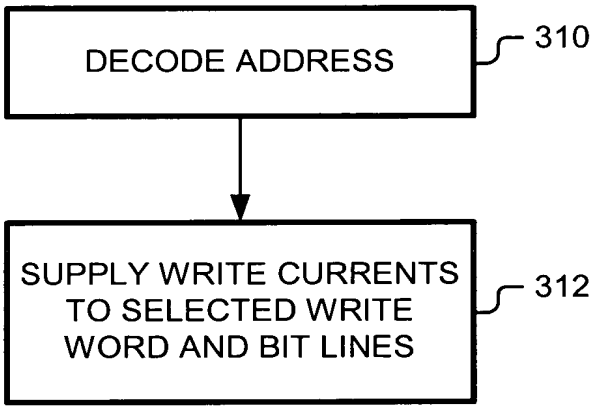

DECODE ADDRESS
310
SUPPLY WRITE CURRENTS TO SELECTED WRITE WORD AND BIT LINES
312

FIG. 6

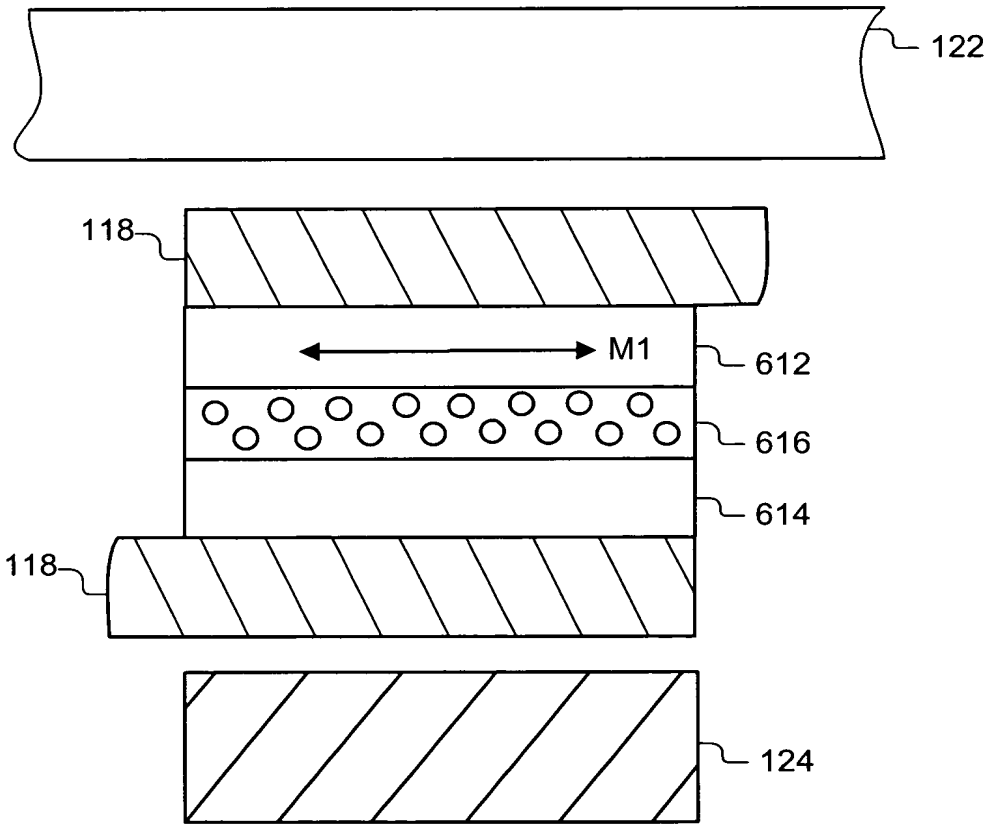

122
118
M1
612
616
614
118
124

FIG. 4

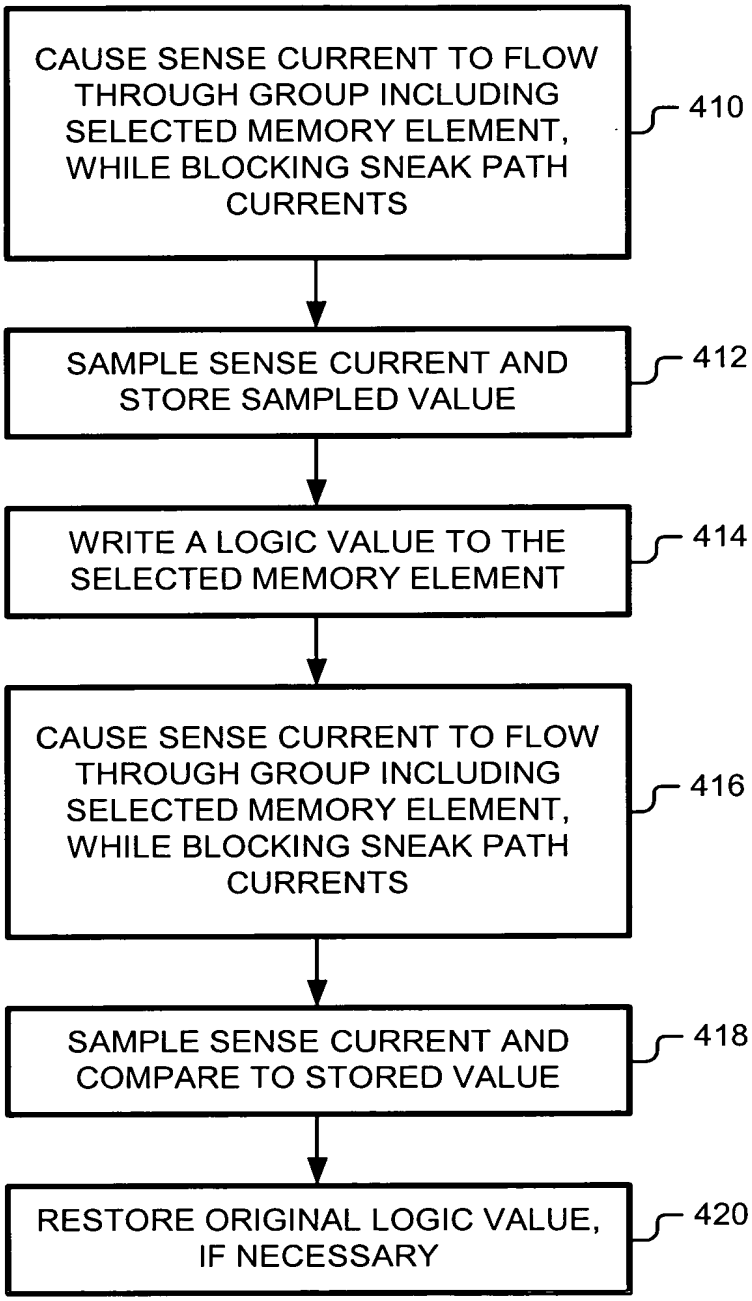

CAUSE SENSE CURRENT TO FLOW THROUGH GROUP INCLUDING SELECTED MEMORY ELEMENT, WHILE BLOCKING SNEAK PATH CURRENTS
410
SAMPLE SENSE CURRENT AND STORE SAMPLED VALUE
412
WRITE A LOGIC VALUE TO THE SELECTED MEMORY ELEMENT
414
CAUSE SENSE CURRENT TO FLOW THROUGH GROUP INCLUDING SELECTED MEMORY ELEMENT, WHILE BLOCKING SNEAK PATH CURRENTS
416
SAMPLE SENSE CURRENT AND COMPARE TO STORED VALUE
418
RESTORE ORIGINAL LOGIC VALUE, IF NECESSARY
420

FIG. 5

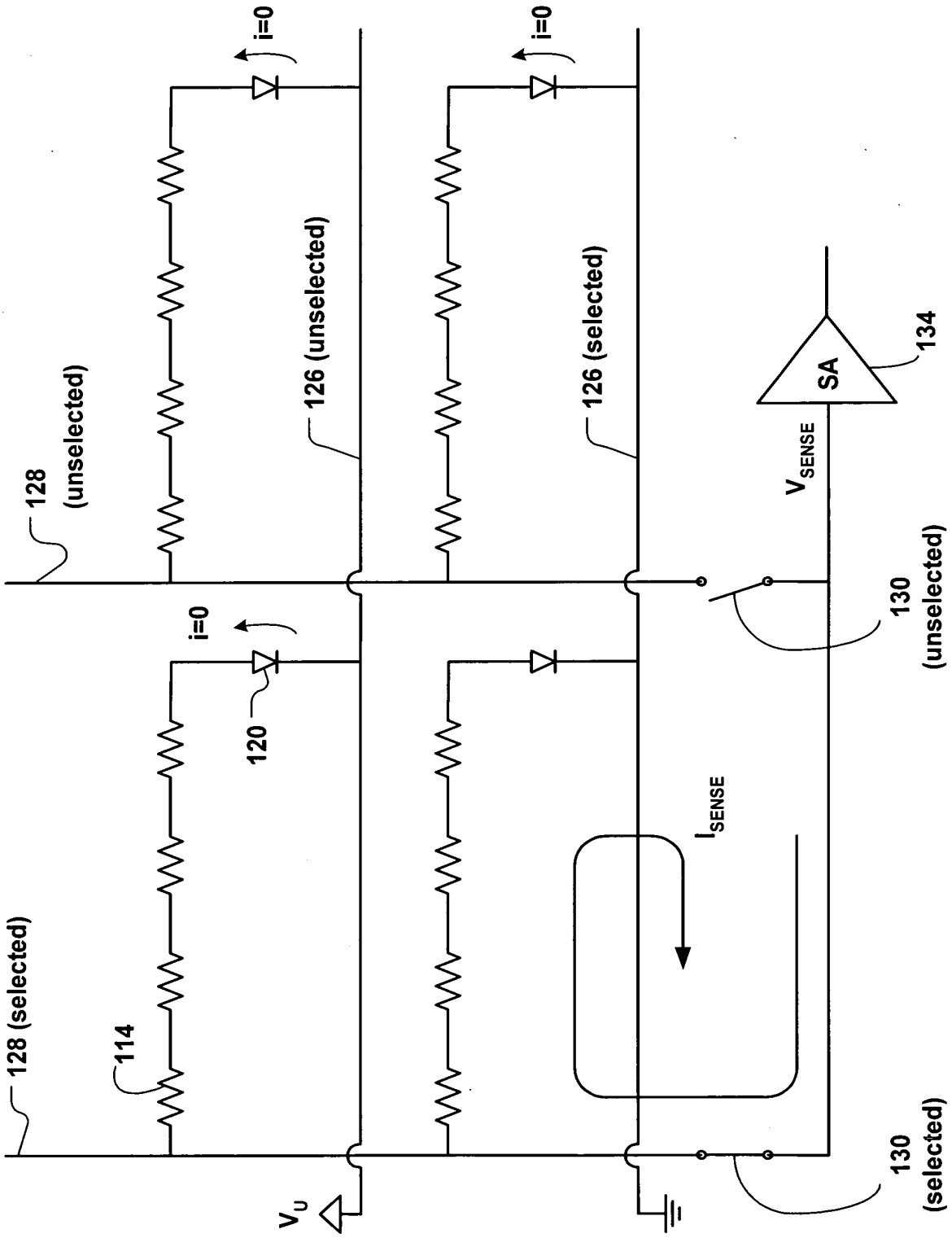
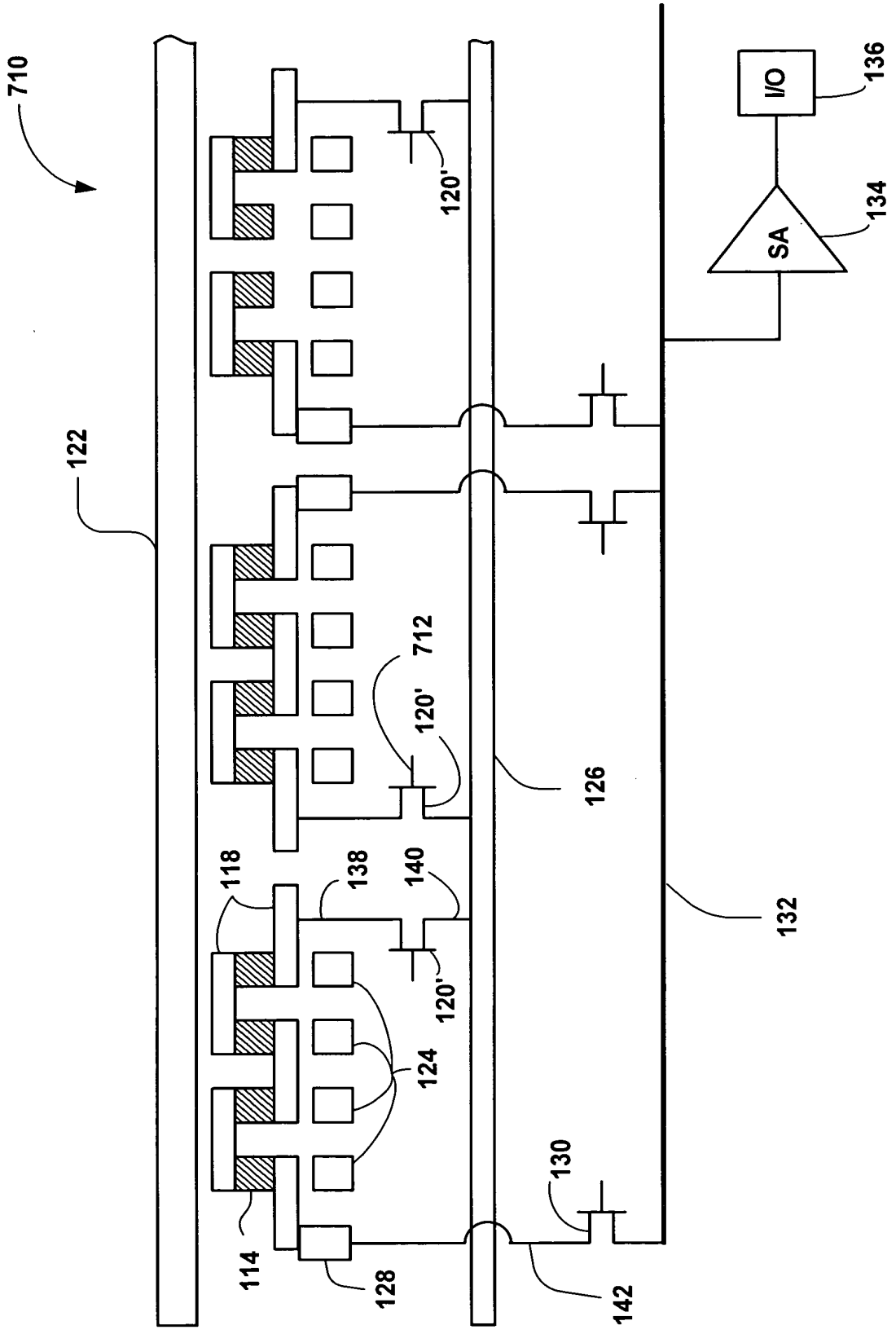

i=0
i=0
i=0
128 (unselected)
126 (unselected)
126 (selected)
SA
134
V_{SENSE}
130 (unselected)
120
128 (selected)
114
I_{SENSE}
V_U
130 (selected)

FIG. 7


710
122
120'
I/O
136
SA
134
712
120'
126
118
138
140
120'
132
124
130
114
128
142